

Customer No.: 31561  
Application No.: 10/605,056  
Docket No.: 9388-US-PA

### **AMENDMENT**

Please amend the application as indicated hereafter.

#### **In the Claims :**

1. (original) A packaging process, comprising the steps of:  
providing an insulation layer, wherein the insulation layer has an upper surface and a corresponding lower surface;  
forming a plurality of openings passing through the insulation layer;  
attaching a tape onto the lower surface of the insulation layer;  
forming a conductive body inside the openings;  
mounting a chip over the upper surface of the insulation layer and the chip electrically connected with the conductive body; and  
removing the tape.
2. (original) The packaging process of claim 1, wherein forming the conductive body inside the openings comprises electroless plating.
3. (original) The packaging process of claim 1, wherein the conductive body has a die pad and a plurality of contacts, and the chip has an active surface, a corresponding backside and a plurality of chip contacts on the active surface.
4. (original) The packaging process of claim 3, wherein mounting the chip over the upper surface of the insulation layer comprises attaching the backside of the chip onto the

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die pad, electrically connecting the chip contacts with the contacts of the conductive body via a plurality of conductive wires, and forming a packaging material to encapsulate the chip and the conductive wires.

5. (original) The packaging process of claim 1, wherein mounting the chip over the upper surface of the insulation layer comprises mounting the chip over the upper surface of the insulation layer via a plurality of bumps, the bumps electrically connecting the conductive body with the chip.

6. (original) The packaging process of claim 5, wherein after mounting the chip over the insulation layer through the bumps, an insulation material is filled into the space between the chip and the insulation layer and the bumps are enclosed by the insulation material.

7. (original) The packaging process of claim 1, wherein before mounting the chip over the upper surface of the insulation layer, a multi-layer substrate is formed by further conducting the steps comprising:

forming an additional insulation layer over the insulation layer;

forming a plurality of openings passing through the additional insulation layer;

and

forming an additional conductive body inside the openings of the additional insulation layer.

8. (original) The packaging process of claim 1, wherein before mounting the chip over the upper surface of the insulation layer, a multi-layer substrate is formed by repeating the steps comprising:

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forming an additional insulation layer over the insulation layer;  
forming a plurality of openings passing through the additional insulation layer;  
and  
forming an additional conductive body inside the openings of the additional insulation layer

9. (original) The packaging process of claim 1, wherein material constituting the conductive body includes copper.

10. (original) The packaging process of claim 1, wherein material constituting the conductive body includes gold.

11. (original) The packaging process of claim 1, wherein the conductive body is a composite structure comprising multiple metallic layers.

12. (original) The packaging process of claim 1, wherein the conductive body is a composite structure comprising a gold layer, a palladium layer, a nickel layer and a palladium layer.

13. (original) The packaging process of claim 1, wherein material constituting the insulation layer is selected from a group consisting of glass epoxy resin, Bismaleimide-Triazine, polyimide and epoxy resin

14.-31. (cancelled).